

CIRCUIT CONFIGURATION AND DISPLAY ELEMENT

Background of the Invention:

Field of the Invention:

5 The invention relates to a circuit configuration having an integrated semiconductor element and a display element. The invention also relates to an integrated display element for diagnosing and displaying a disturbance in a semiconductor element.

10 Semiconductor-based circuit breakers such as, for example, intelligent power semiconductors ("Smart Power Switches") that are used to drive a load can be equipped with display elements for displaying the circuit state, for example "ON" or "OFF". A generally known example of such a circuit configuration for  
15 displaying the circuit state is illustrated in Fig. 1. In the generally known illustrated circuit configuration for displaying an item of diagnostic information, a light-emitting diode (LED) which shines in a green color and has a series resistor in parallel with the load, is disposed at the output  
20 of the power stage.

Furthermore, intelligent power semiconductors such as those sold under the trademark PROFET® by Infineon Technologies AG, are known; these intelligent power semiconductors have diagnostic outputs for outputting diagnostic functions. Items  
5 of information to be output can be, for example, the occurrence of a short circuit, of overtemperature of the power stage, of a load disconnection, or of something similar.

It is disadvantageous in the known intelligent power semiconductors with diagnostic outputs to require a  
10 comparatively high number of electronic components to display the information to be diagnosed because these electronic components must be connected to the intelligent semiconductor element.

Summary of the Invention:

15 It is accordingly an object of the invention to provide a circuit configuration and display element that overcomes the hereinafore-mentioned disadvantages of the heretofore-known devices of this general type and that reduces the circuitry required to display the diagnostic function in semiconductor  
20 elements.

With the foregoing and other objects in view, there is provided, in accordance with the invention, a circuit

configuration. The circuit configuration includes an intelligent power semiconductor having a single diagnostic output, a logic element, and a power switch. The power switch switches a load, a current source, and a current sink. The load, the current source, and the current sink connect to a single diagnostic output and provide a current at the diagnostic output. The logic element can control the power switch and/or the current source and the current sink. The circuit configuration also includes a display element having light-emitting semiconductor diodes, a first terminal, and a second terminal. The first terminal electrically connects to the single diagnostic output of the semiconductor element. The second terminal electrically connected to the load output of the power switch. The light-emitting semiconductor diodes connects in antiparallel between the first terminal and the second terminal and outputs different visual information items when receiving different currents.

With the objects of the invention in view, there is also provided a display element for diagnosing and displaying a disturbance in an intelligent power semiconductor. The display element includes an input terminal for connecting to an intelligent power semiconductor. The display element also includes a supply terminal for receiving a supply potential. The display element includes light-emitting semiconductor

diodes connected in antiparallel between the input terminal and the supply terminal. The light-emitting semiconductor diodes output differing light signals as a function of a disturbance of the intelligent power semiconductor upon receiving a diagnostic signal fed through the input terminal.

In accordance with a further object of the invention, provision is made of a circuit configuration having an integrated semiconductor element that has a power stage for switching a load, a current source, and a current sink, which are connected to a single diagnostic output and which provide a current at the diagnostic output. The circuit configuration includes a drive circuit for controlling the power stage and/or the current source and current sink. The drive circuit has a display element with a first terminal that is electrically connected to the diagnostic output of the semiconductor element. The diagnostic output of the semiconductor element has a second terminal that is electrically connected to the load output of the power stage and that contains light-emitting semiconductor diodes. The light-emitting semiconductor diodes are connected in antiparallel and output to their environment visual information items, which can be distinguished in accordance with the current fed to them.

Depending on the type of the multiplicity of information desired, either one or more current sources and/or current sinks are integrated in the semiconductor element. The display elements, which can be LEDs, for example, can be fed directly electrically by the use of current sources or current sinks.

In accordance with the invention, the electric connection to the display element is made via precisely one line. The diagnostic function can be achieved without the need for additional electric components to connect the display element.

If the semiconductor element being used is a semiconductor element with an integrated logic circuit and/or with a drive circuit, it is expedient for the current source or current sink and/or the power stage to be driven by the logic circuit.

In an advantageous development, the intensity and flow direction of the current supplied at the diagnostic output by the current source and current sink provide an item of information on the status of the power stage, the load connected to the power stage, or the semiconductor element.

The current source connects to the current sink for this purpose in a fashion capable of electric conduction to at least one diagnostic output of the semiconductor element. The

diagnostic output serves to signal an item of information on the status of the circuit configuration and/or of a load connected to the semiconductor element. The following can be provided, for example, as items of status information:

1. The semiconductor element and the power stage are operating in normal mode.
2. The semiconductor element and the power stage are switched off.
3. The semiconductor element and the power stage and/or the load connected thereto exhibit a fault, a disturbance, a malfunction or a functional failure. Of course, still further items of status information could also be conceivable.

In a refinement, which is typical in terms of circuitry, the current source and the current sink form a half-bridge configuration relative to one another. The center tap of the half bridge connects to the diagnostic output. The current source and/or current sink are/is preferably constructed as MOSFETs.

Typically, in addition to the diagnostic output, the semiconductor element also has an input terminal, an output terminal connected to the load, and at least one supply terminal for a supply potential. Typically, in addition to the terminals for the diagnosis, the input and the output, the diagnostic element has at least one terminal for a supply potential. Typically, in addition to the terminals for the

diagnosis, the input and the output, the diagnostic element has at least one terminal for a supply potential.

At least one of the abovementioned current sources or current sinks preferably connects to the input terminal of the semiconductor element in such a manner that the connection is immediately capable of electric conduction.

In a very advantageous refinement, the power stage embodies a low-side switch. The semiconductor element then has only a single supply terminal at which a first supply potential is present. A second supply potential is impressed here via the output terminal, which connects to the load. It is possible in this way to dispense with one terminal of the semiconductor element.

In accordance with a further preferred embodiment of the invention, the semiconductor element is connected to a load whose first terminal is connected to a negative supply potential. The load path of the semiconductor element is disposed between a positive supply potential and the load output. Such configurations are also known as high-side switches.



The power stage is preferably a power semiconductor component. The power semiconductor component is preferably configured as a power MOSFET or as an IGBT since these components are particularly well suited for switching high currents and/or  
5 voltages.

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The invention also relates to an integrated display element for diagnosing and displaying a disturbance in a semiconductor element. In diagnosing and displaying a disturbance, the display element has light-emitting semiconductor diodes which  
10 are connected in antiparallel, an input terminal, and at least one supply terminal for a supply potential. In accordance therewith, a diagnostic signal is fed via the input terminal. Then, the semiconductor diodes output light signals of different intensity and/or color to their environment as a  
15 function of a disturbance of the semiconductor element.

The display element preferably emits light of different wavelength spectra, in particular different colors.

Preferably, the wavelength spectra emitted are a function of the current flow and of the direction of the current flow in  
20 order to distinguish the type of information items to be displayed.



The display element can include, for example, a green and an orange LED. The green LED then shines in normal mode, the orange LED shines in the event of malfunction, and no LED shines in the switched-off mode. A two-color light-emitting diode can also be used in a particularly expedient fashion. Blue, red, or white LEDs would also be conceivable in addition or alternatively.

In a typical development, the display element contains at least one optocoupler. At least one light-emitting semiconductor diode is advantageously also integrated in the optocoupler. It is also possible, and this is particularly expedient in the case of circuits with electrically isolated power section and logic section, for the display elements to be functional constituents of other electronic components such as, preferably, optocouplers. If optocouplers are used, an electrical isolation of the power section from the logic section results easily, as is required, for example, by applications in a switch-mode power supply, a power supply, in a field bus driver, etc.

Other features which are considered as characteristic for the invention are set forth in the appended claims.

Although the invention is illustrated and described herein as embodied in circuit configuration and display element, it is nevertheless not intended to be limited to the details shown, since various modifications and structural changes may be made  
5 therein without departing from the spirit of the invention and within the scope and range of equivalents of the claims.

The construction and method of operation of the invention, however, together with additional objects and advantages thereof will be best understood from the following description  
10 of specific embodiments when read in connection with the accompanying drawings.

Brief Description of the Drawings:

Fig. 1 is a partial schematic and partial diagrammatic view showing a circuit configuration and a semiconductor element  
15 with a diagnostic device according to the prior art;

Fig. 2 is a partial schematic and partial diagrammatic view showing a first exemplary embodiment of a semiconductor element according to the invention with a diagnostic device and a diagnostic element connected thereto (high-side switch);

20 and

Fig. 3 is a partial schematic and partial diagrammatic view showing a second exemplary embodiment of a semiconductor element according to the invention with a diagnostic element (low-side switch).

5 Description of the Preferred Embodiments:

In all the figures of the drawing, sub-features and integral parts that correspond to one another bear the same reference symbol in each case.

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10 Referring now to the figures of the drawings in detail and first, particularly to Fig. 1 thereof, there is shown a circuit configuration in which an integrated intelligent power semiconductor 1 (Smart Power Switch) is connected to a display element 7 for the diagnosis. The power semiconductor 1 includes a logic element 5 and a power switch 6 (for example a  
15 MOSFET) connected to the logic element 5. The power semiconductor 1 also has terminals for a supply potential  $V_{bb}$ , for the output OUT, and for a reference potential GND. The output OUT is connected to a load  $R_L$ . Connected in parallel with the load  $R_L$  is a display element 7, which must in general  
20 be matched with a series resistor 8.

During operation of the circuit configuration according to the prior art, the display element 7 signals the circuit state of

the power semiconductor 1. The display element shines if the power switch 6 is in the switched-on state. The display element does not shine in the converse case, when the circuit breaker is in the switched-off state. A malfunction of the power semiconductor 1 cannot be signaled in the case of the circuit configuration according to Fig. 1.

The circuit configuration in Fig. 2 shows an example in accordance with the invention, having an intelligent power semiconductor 1 and a display element 11 connected to the power semiconductor 1. The power semiconductor 1 includes a logic element 5 and a MOSFET 6 connected to the logic element 5. Also provided are terminals for a supply potential  $V_{bb}$ , for the output OUT, and for a reference potential GND. Connected to output OUT is the load  $R_L$ , a node K1 being provided in accordance with the invention for the terminal of a display element 11. According to the invention, the display element 11 is directly connected to an additionally provided diagnostic output DIAG of the semiconductor element, thus yielding a full H-bridge. The diagnostic output DIAG 2 is supplied by a current source 3 and a current sink 4 such that the display element 11 no longer requires a series resistor. The current source 3 and the current sink 4 connected as a half bridge are driven by the logic element 5. The first terminal of the current source 3 is connected to the supply

potential  $V_{bb}$ . The second terminal of the current source 3 leads to the diagnostic output DIAG 2 and to the first terminal of the current sink 4. The second terminal of the current sink 4 is connected to the frame terminal GND.

- 5 The display element 11 includes two LEDs connected in antiparallel: a green shining LED 9 and an orange shining LED 10. The display element 11 has two terminals with the aid of which the functions of the display element 11 can be activated. The first terminal is connected to the diagnostic output DIAG 2. The second terminal is connected to node point K1.

During operation of the circuit configuration, the display element 11 switches the provided signal states as a function of the direction of the current flowing through the display element 11. When no current flows, both LEDs 9, 10 remain off. If the current source 3 is activated, for example, upon the occurrence of a short circuit, a current  $I_{or}$  flows from terminal DIAG 2 to the node K1. The display element 11 or the LED 10 shines orange. If the current  $I_{gr}$  flows in the reverse direction in the case of an activated current sink 4, the display element 11 or the LED 9 shines green. Normal operation, for example, can be displayed thereby. The circuit states are determined by the logic element 5, and so in

addition to the status of the load  $R_L$  it is also possible to signal a malfunctioning of the power semiconductor 1. In the case of a short circuit, the node K1 is drawn to a low potential, and the current for the orange light-emitting diode 10 can flow via current source 3 from the positive supply potential  $V_{bb}$  via the light-emitting diode to terminal GND. The circuit configuration in accordance with Fig. 2, is therefore configured as a high-side switch.

Fig. 3 shows a further example according to the invention for a circuit configuration and for a possible embodiment of an intelligent power semiconductor 1 according to the invention. The display element 11 corresponds to the embodiment illustrated in Fig. 2. The power semiconductor 1 with logic element 5 drives a power switch 6, which is, for example, a MOSFET. Via the input Input, the logic element of the intelligent power semiconductor receives from the outside drive signals for the desired circuit state of the power switch 6.

A terminal for the supply potential  $V_{bb}$  can be eliminated in accordance with the embodiment of Fig. 3. For this purpose, output OUT is connected via node point K1 to the side of the load  $R_L$  with reduced output potential. The load  $R_L$  is connected directly to the positive supply potential  $V_{bb}$ . A

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further connection to the second terminal of the display  
element 11 exists from the node point K1. The first terminal  
of the display element 11 is connected to the diagnostic  
output DIAG 2 of the semiconductor element. The diagnostic  
5 output DIAG 2 is supplied, as in Fig. 3, by a current source 3  
and current sink 4. The current source 3 and the current  
sink 4 are likewise driven by the logic element 5, as in  
Fig. 3. The first terminal of the current source 3 is  
directly connected to the input Input. The second terminal of  
10 the current source 3 leads to the diagnostic output DIAG 2 and  
to the first terminal of the current sink 4. The second  
terminal of the current sink 4 is connected to the terminal  
GND.

The circuit configuration of Fig. 3 is configured as a low-  
15 side switch, and this advantageously leads to a saving of a  
voltage supply terminal Vbb.

The present semiconductor element according to the invention  
(having a diagnostic device, the diagnostic element, and the  
circuit configuration for diagnosing) have been set forth with  
20 the aid of the preceding description so as best to explain the  
principle of the invention and its practical application. Of  
course, the configuration according to the invention can be



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